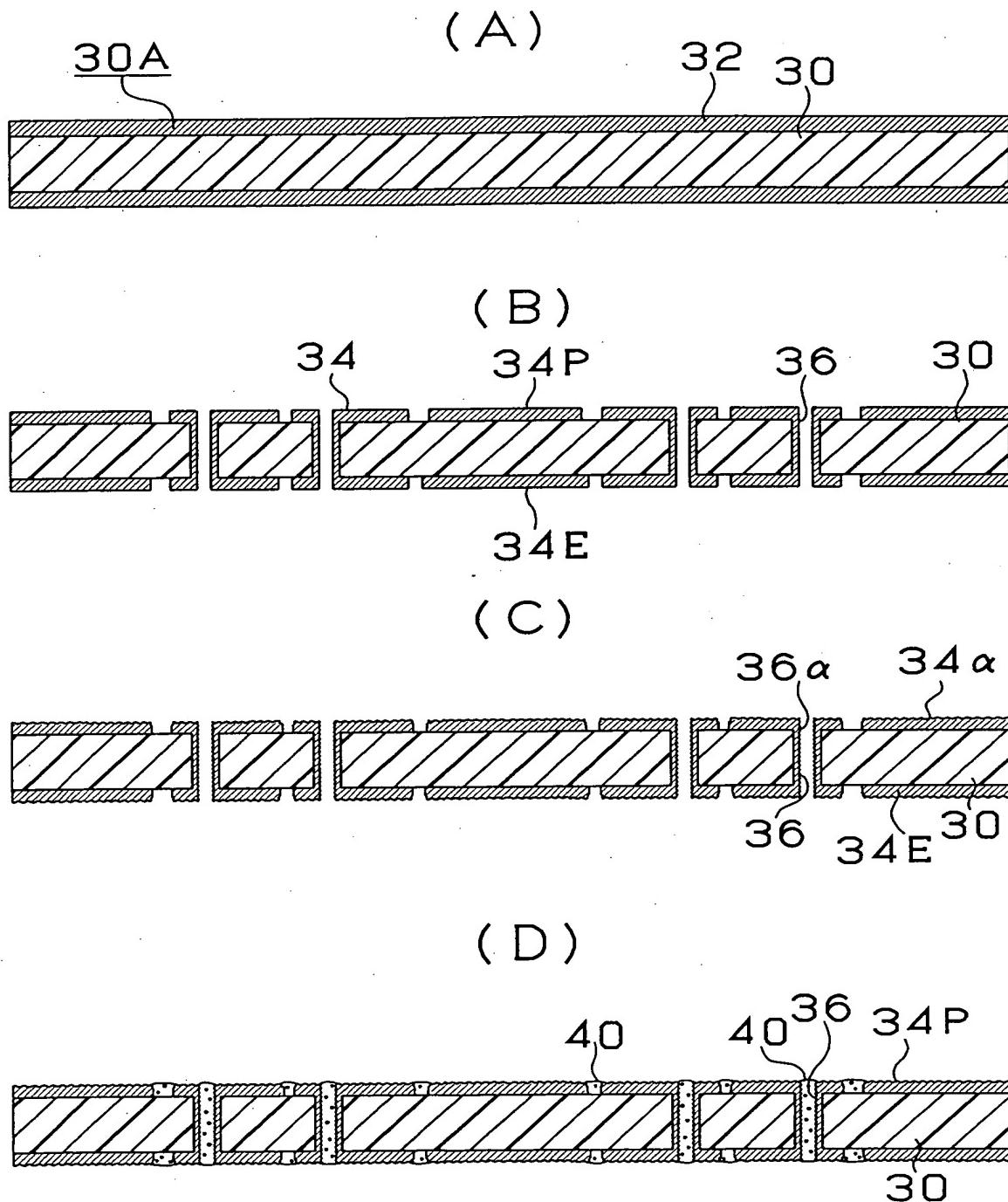


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Fig. 1

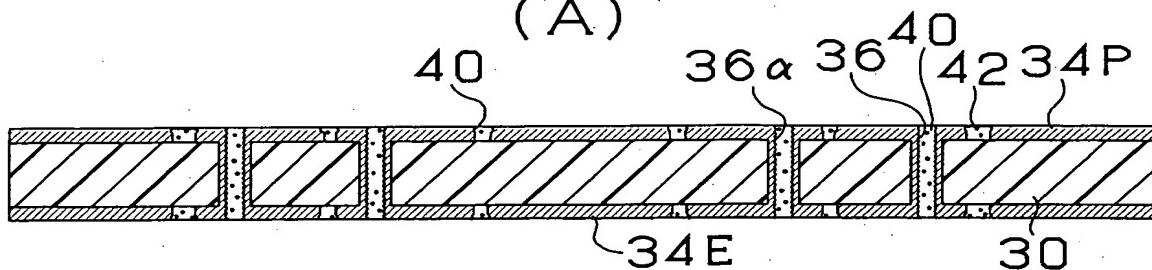


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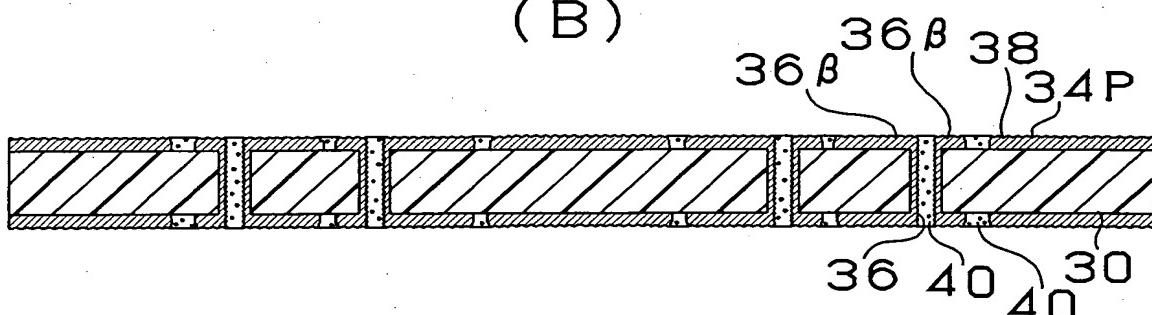
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Fig. 2

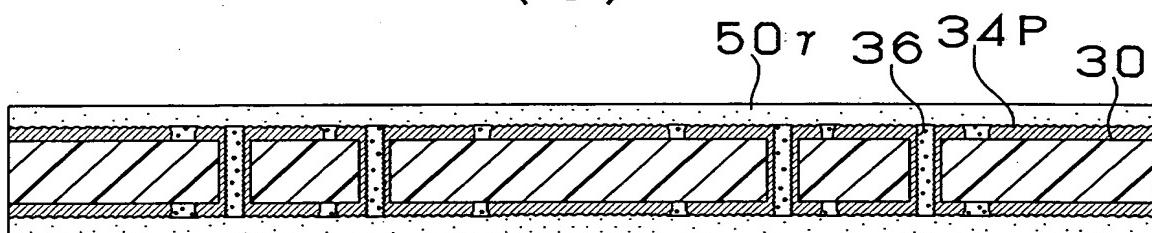
(A)



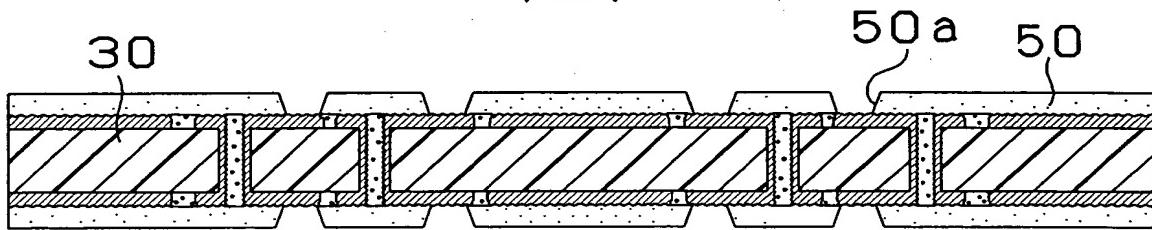
(B)



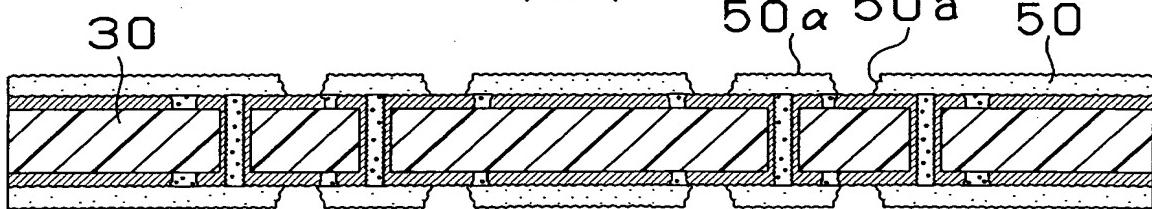
(C)



(D)



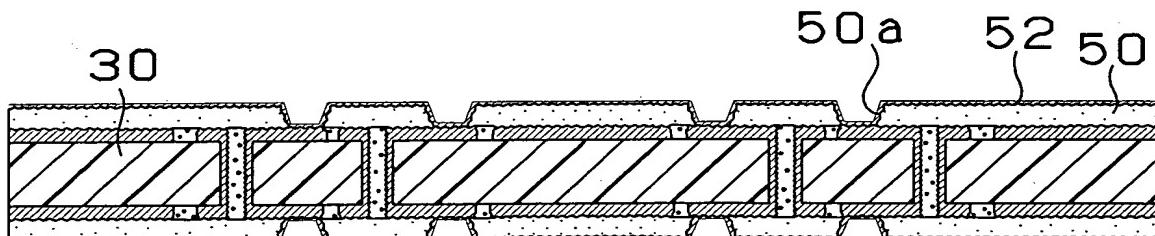
(E)



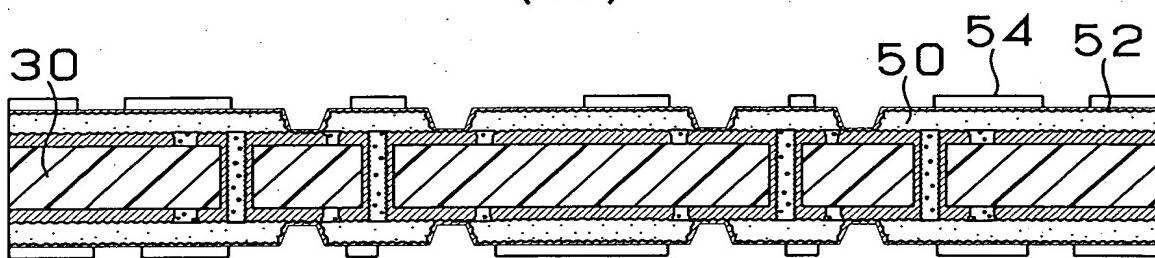
3/29

Fig. 3

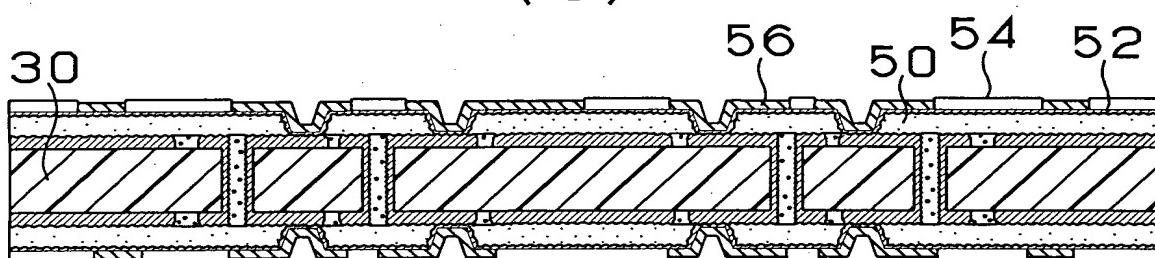
(A)



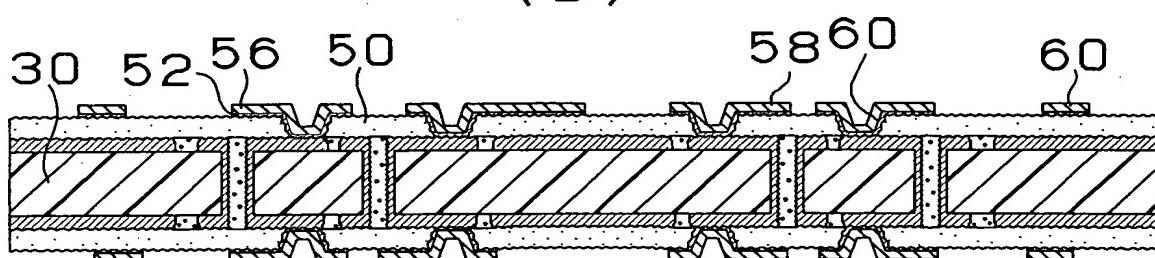
(B)



(C)

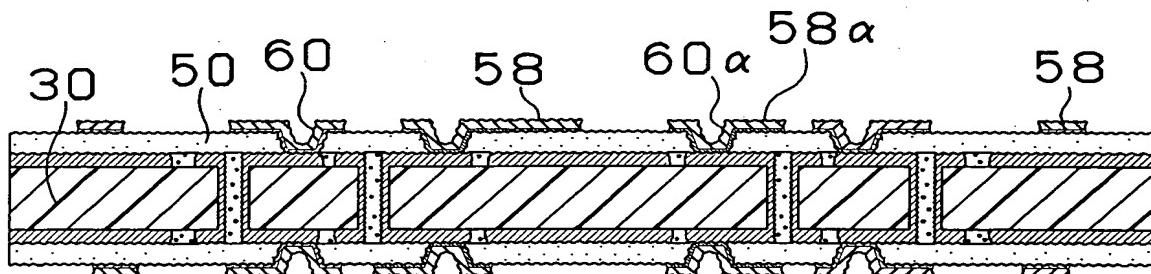


(D)

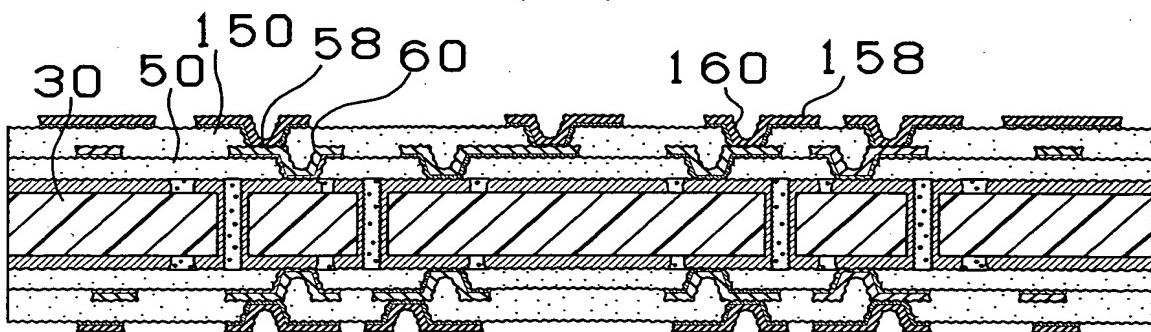


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Fig. 4

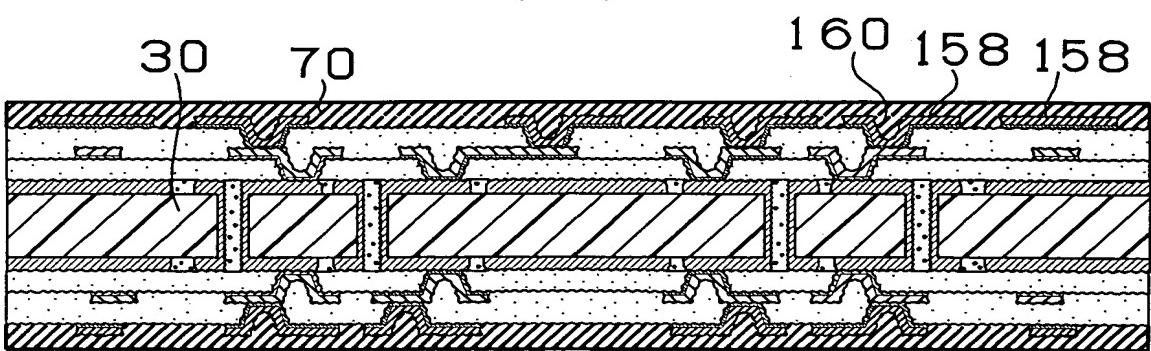
(A)



(B)

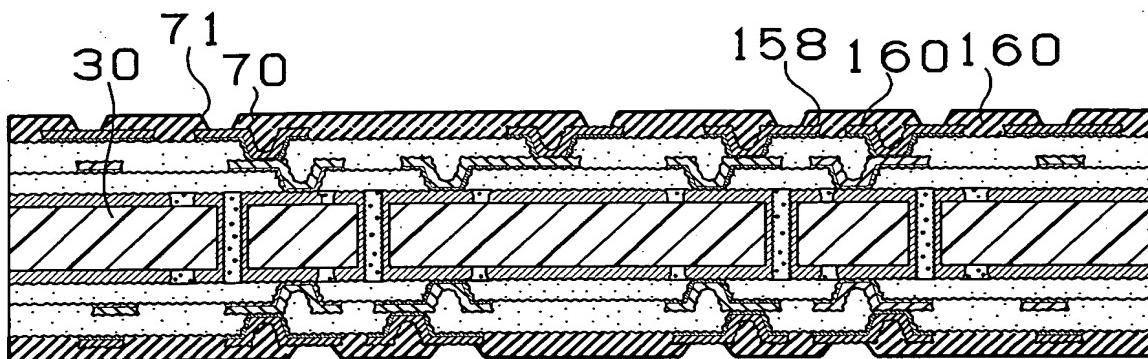


(C)



5/29
Fig. 5

(A)



(B)

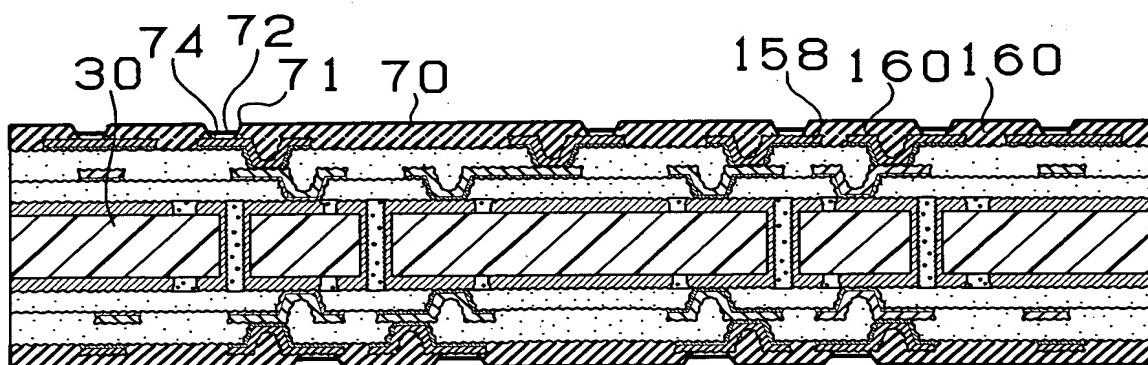


Fig. 6

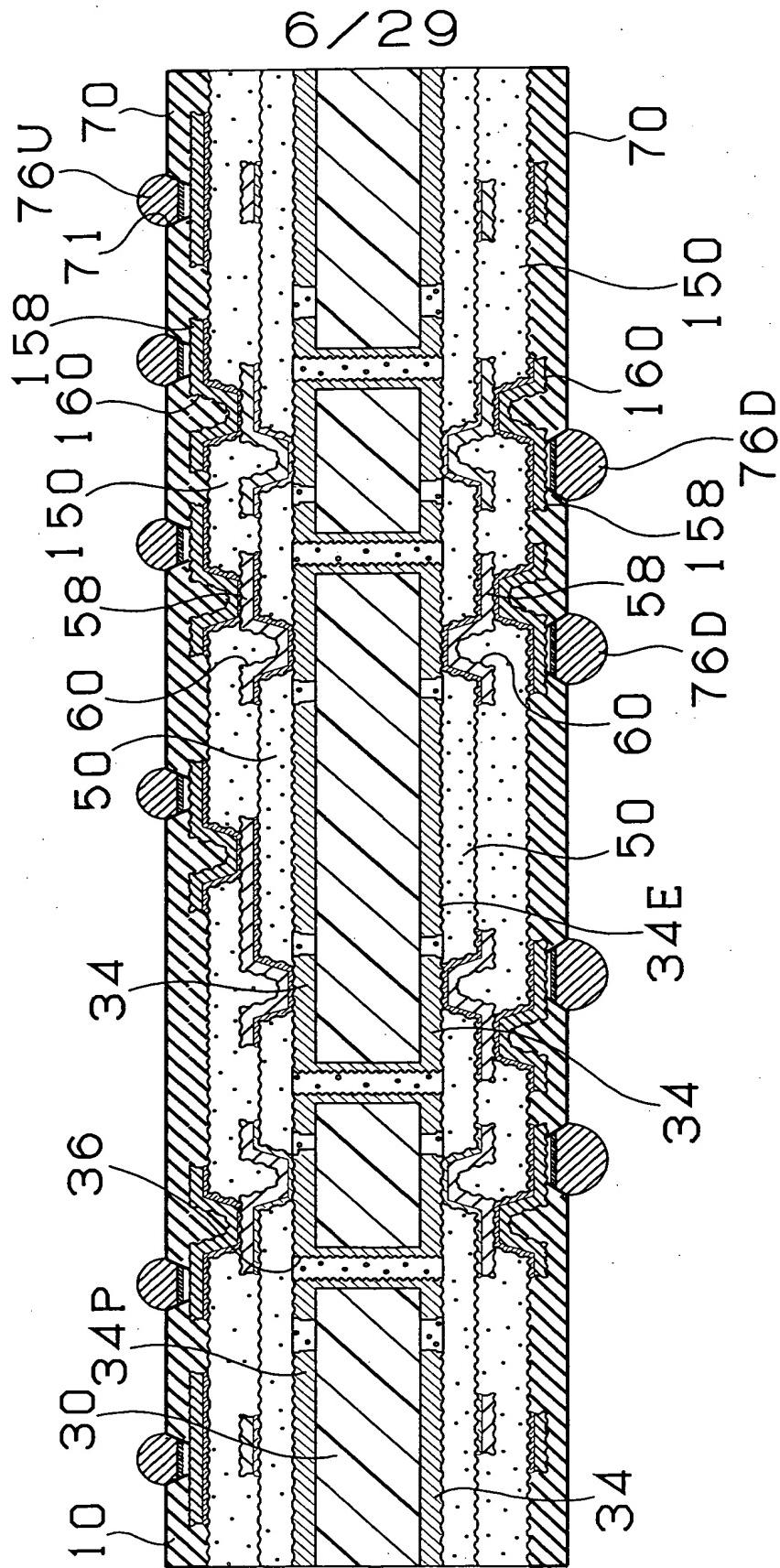
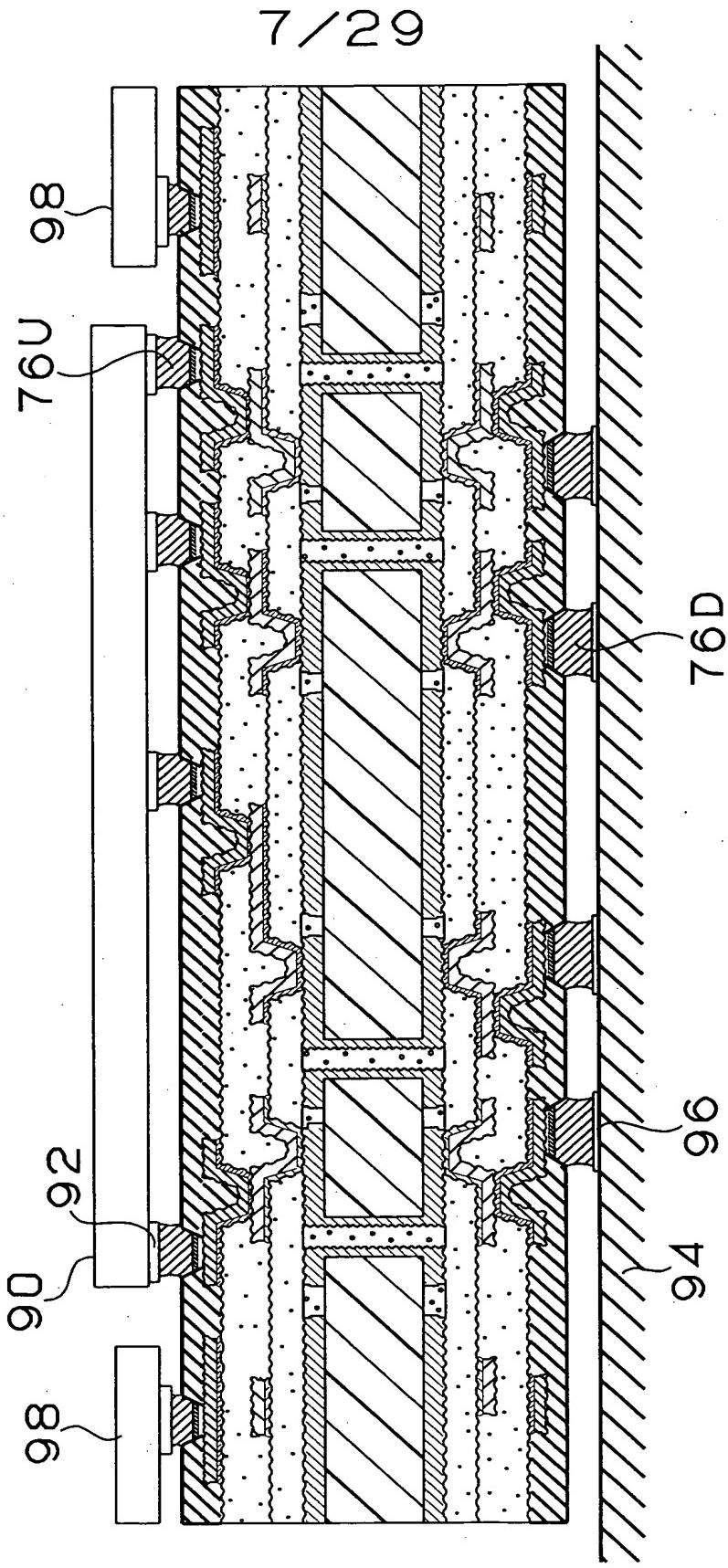
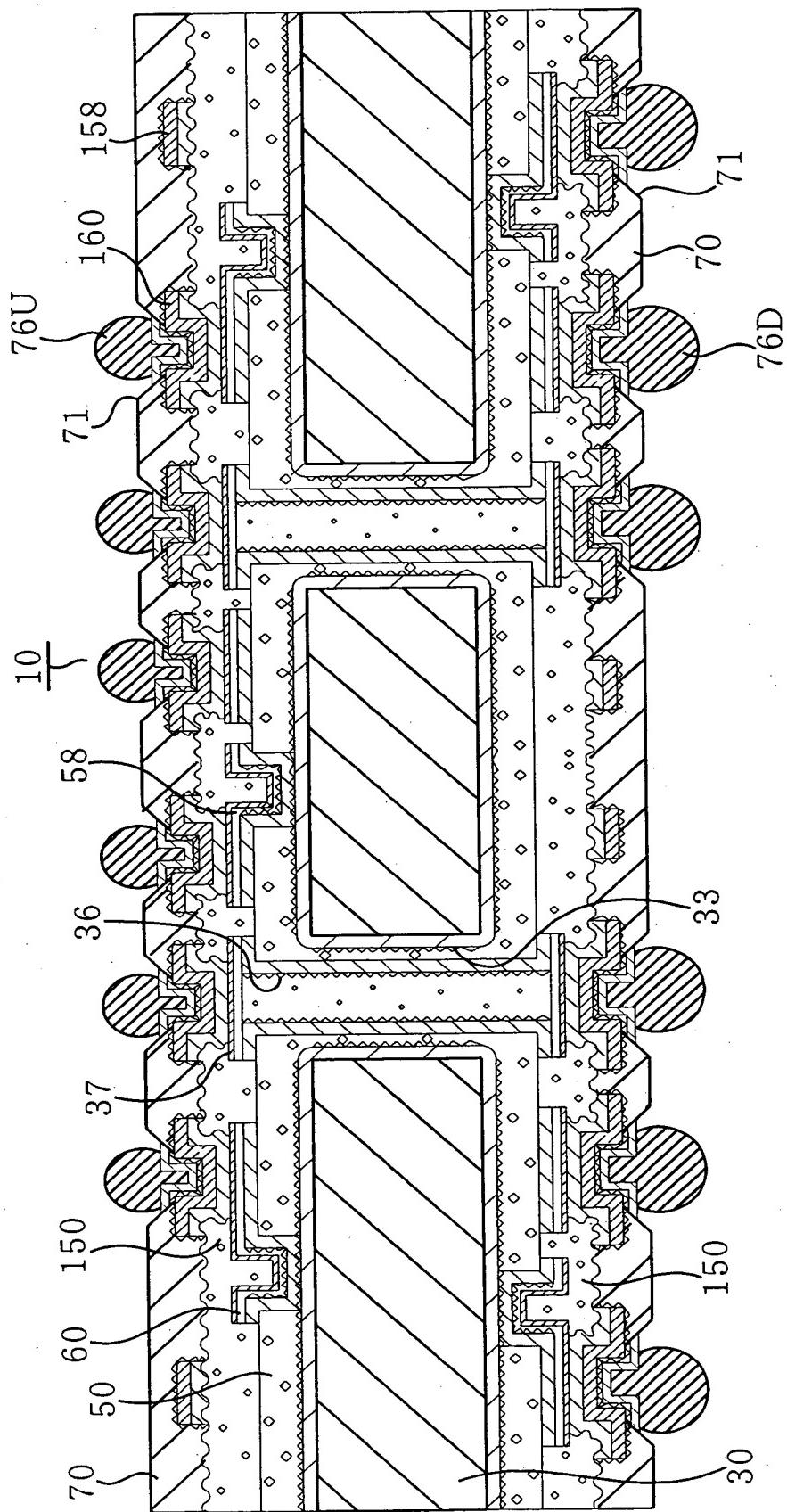


Fig. 7



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Fig. 8



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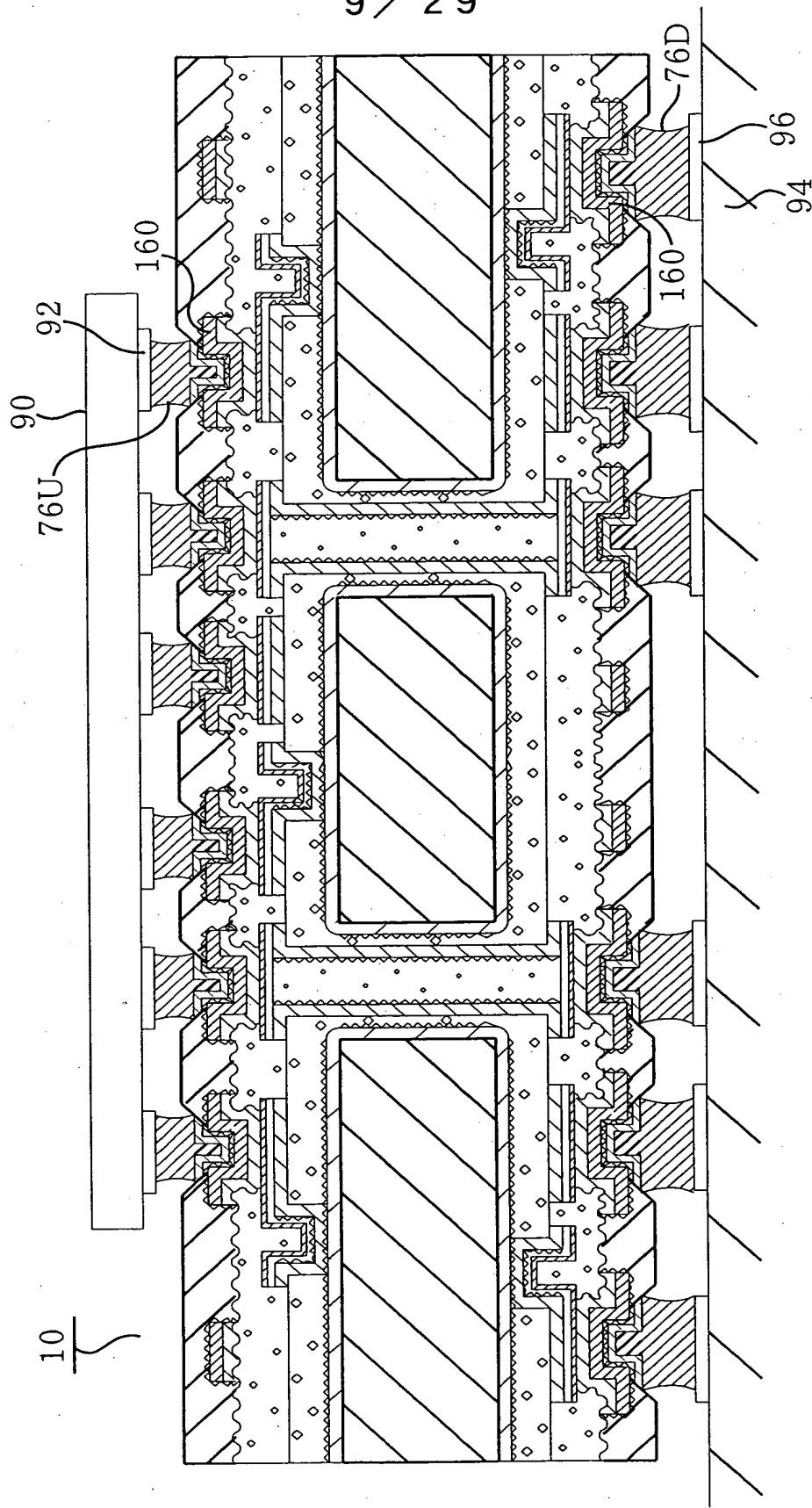


Fig. 9

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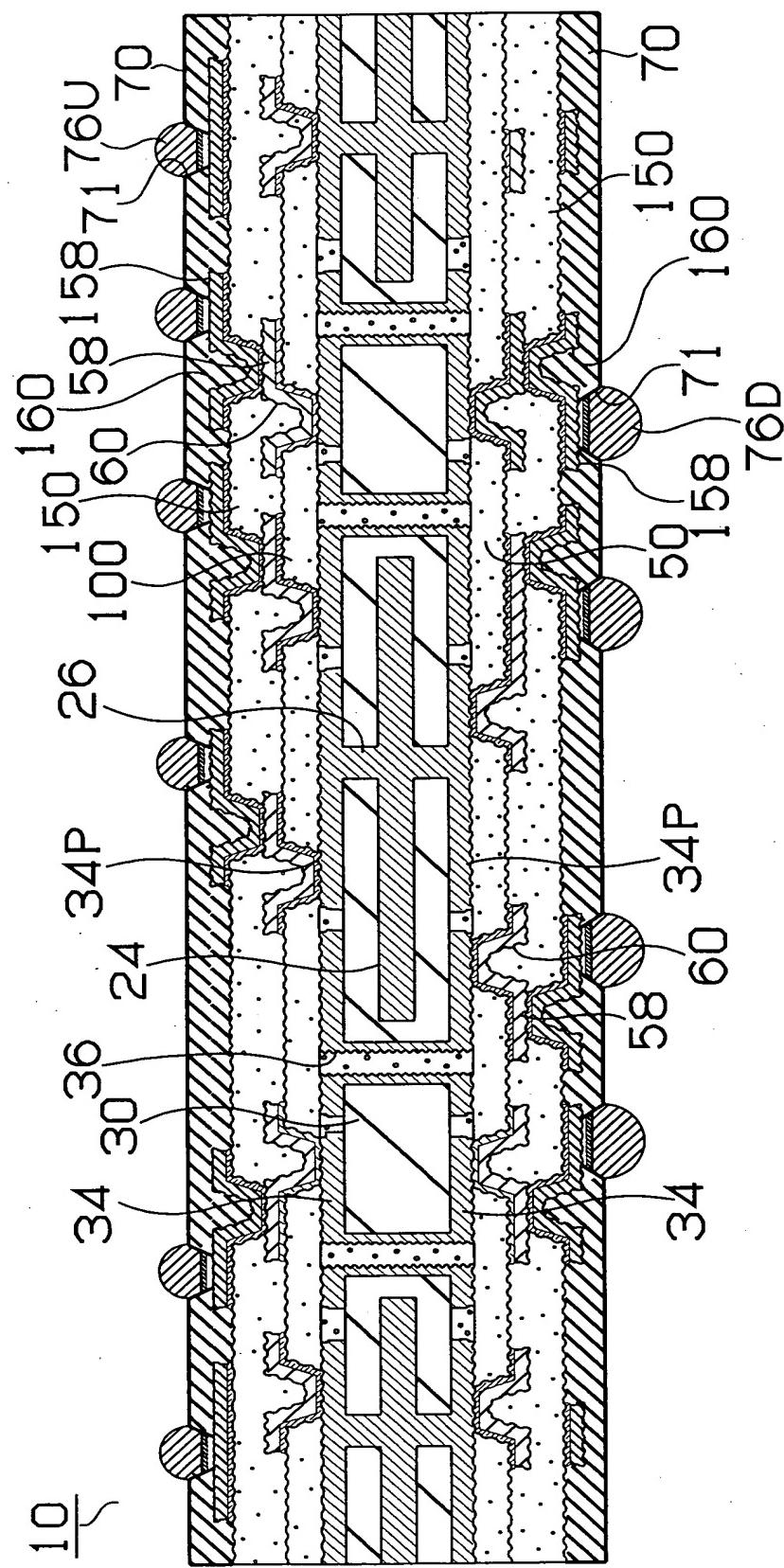
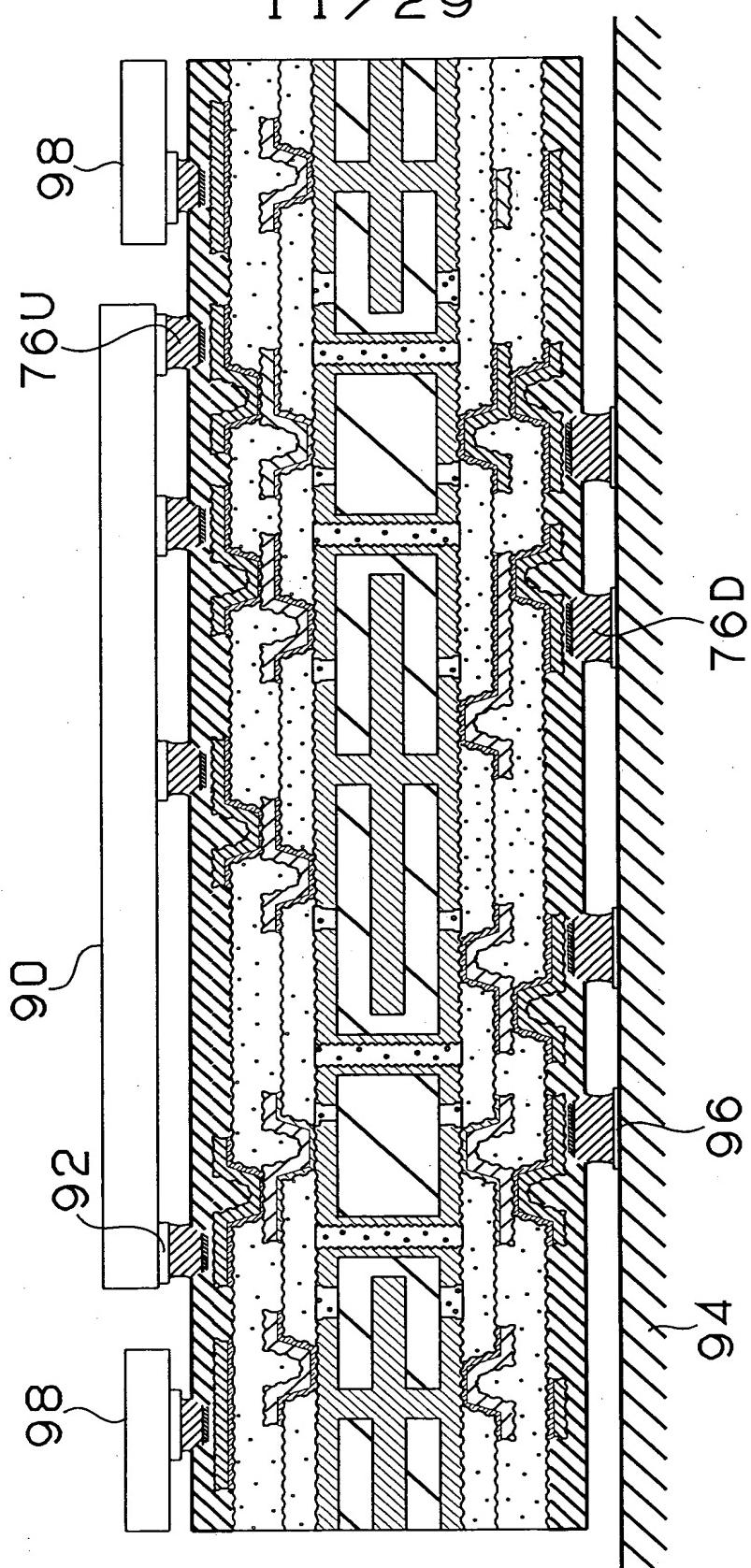


Fig. 10

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Fig. 11



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Fig.12
12 (A)



12a 12 (B)



12a 13 12 (C)



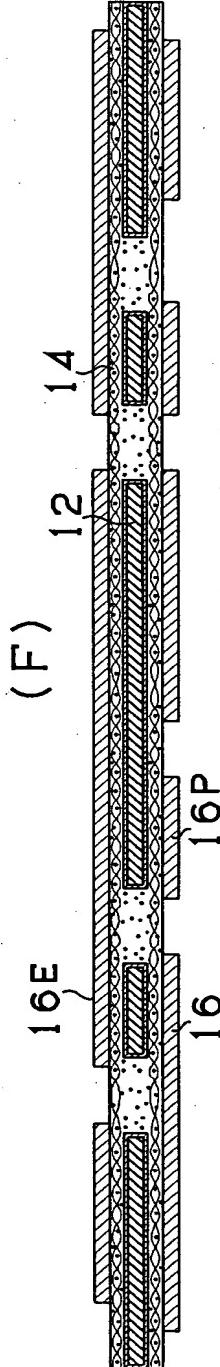
(D)



(E) 12 16 14



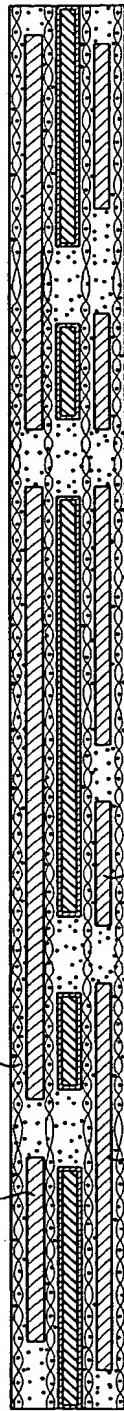
(F)



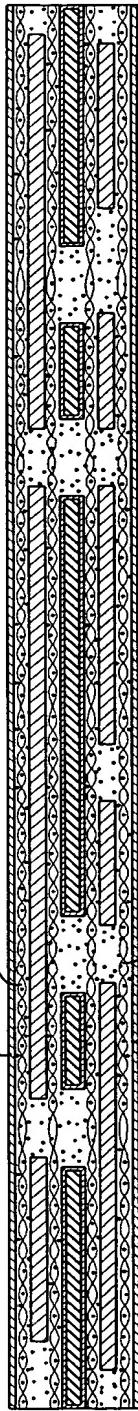
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Fig. 13
(A)

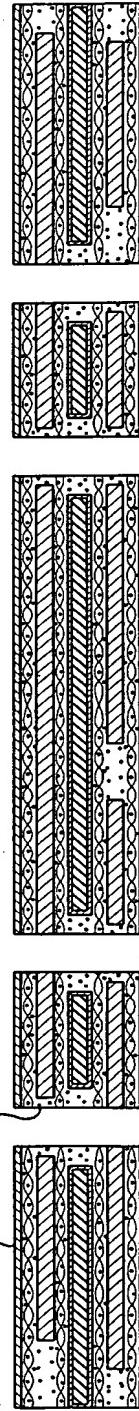
16E 18



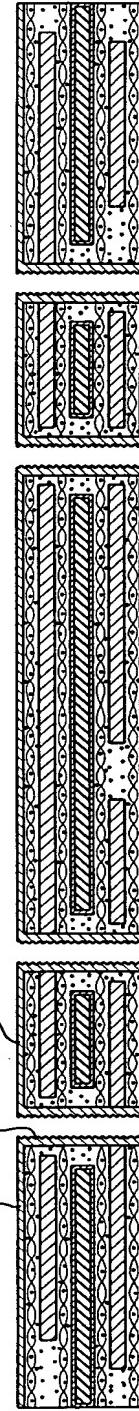
18 16 34a 18 16P (B)



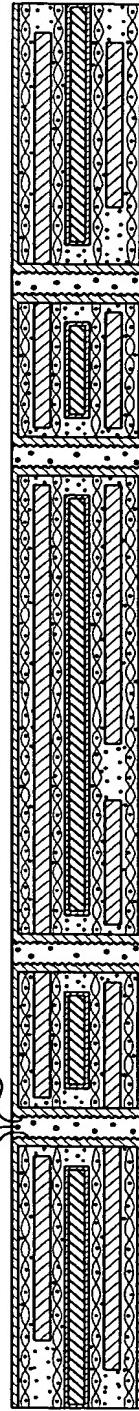
34a 36a 34a 18 (C)



34a 34a 36 22 (D)

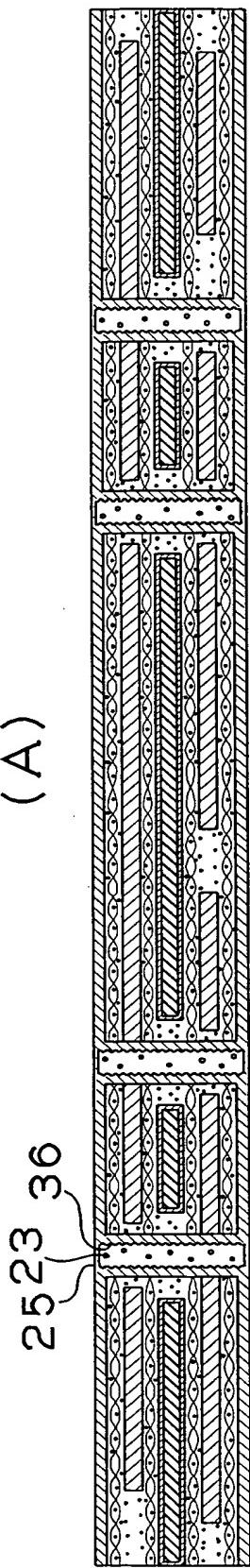


36 23 22 (E)

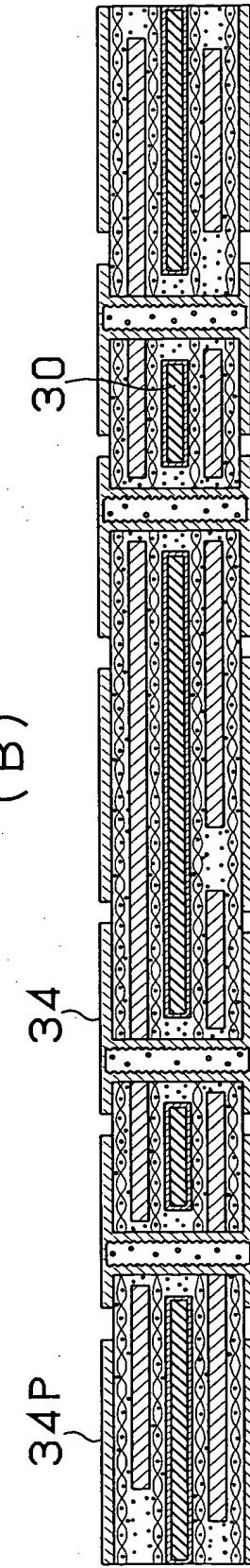


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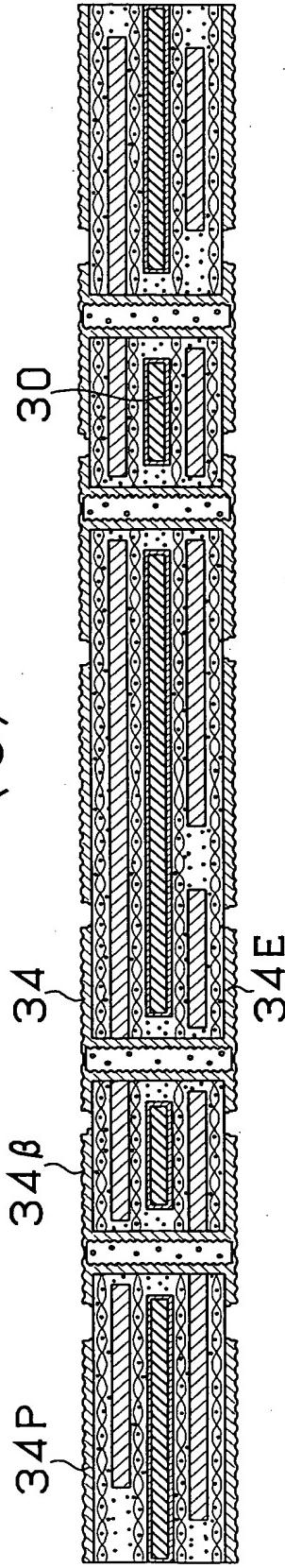
Fig.14
(A)



(B)

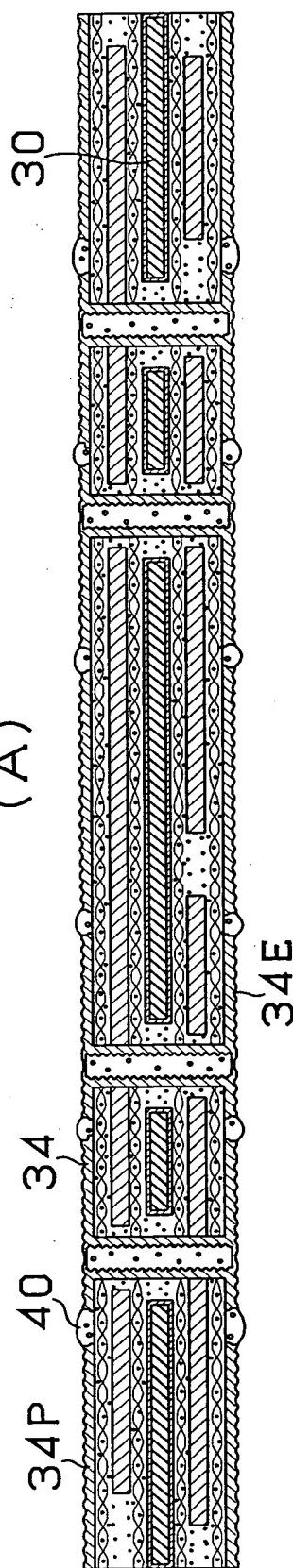


(C)

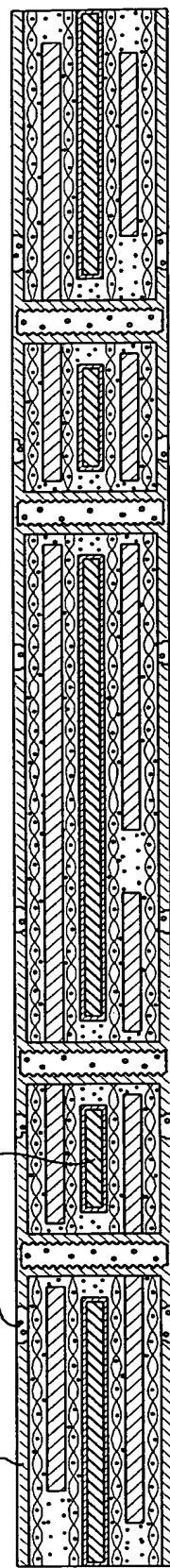


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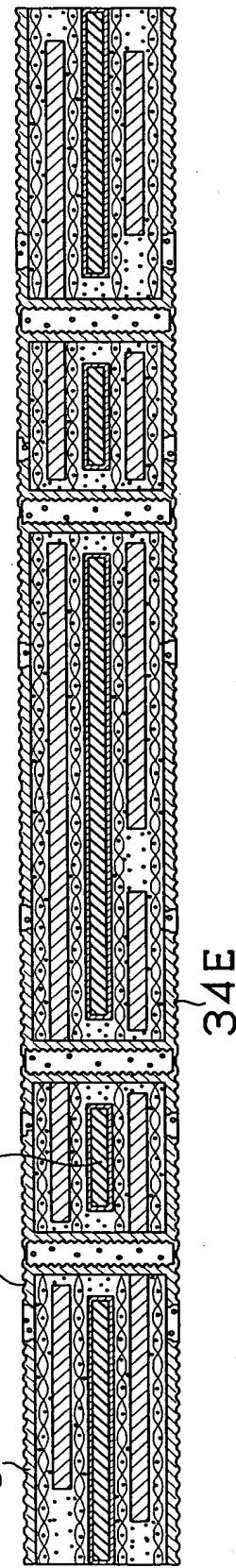
Fig. 15
(A)



(B)

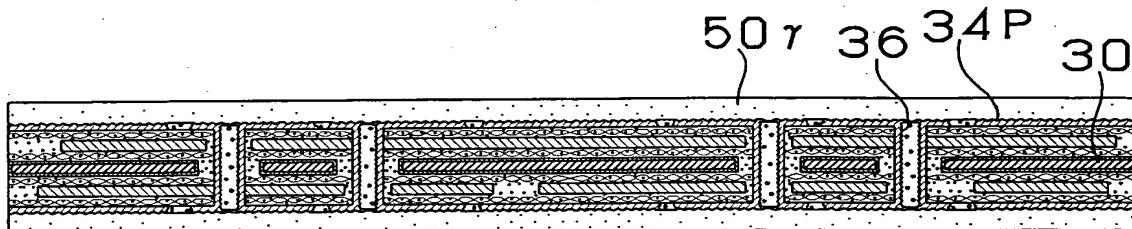


(C)

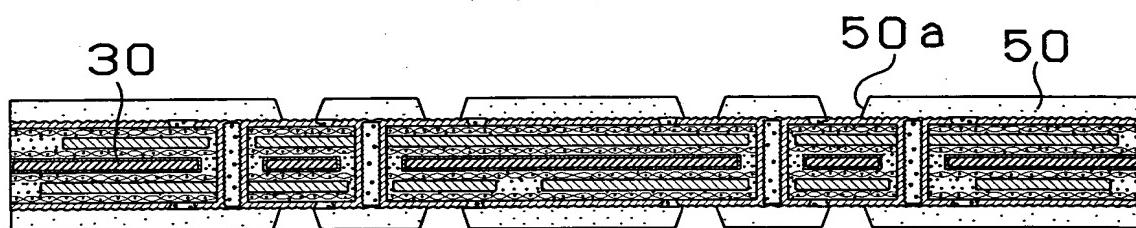


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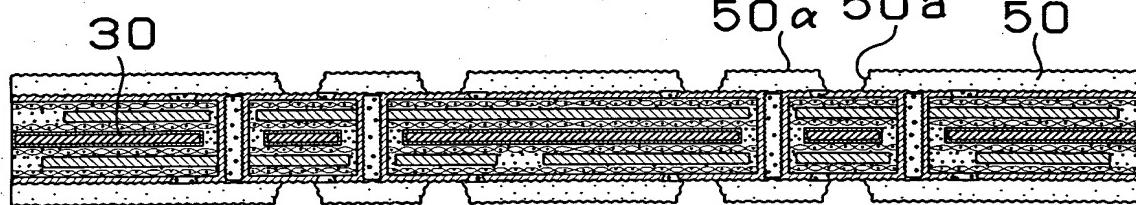
Fig. 16
(A)



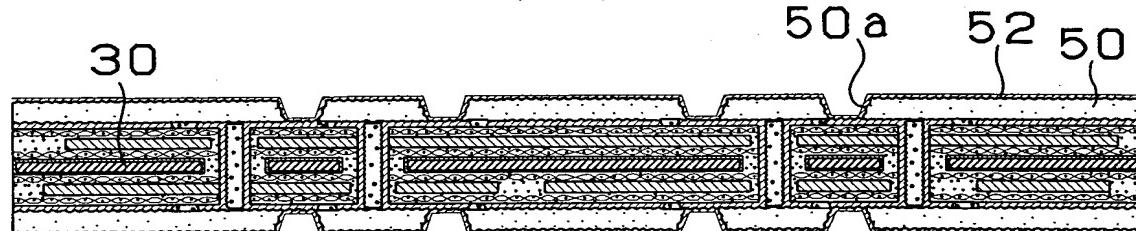
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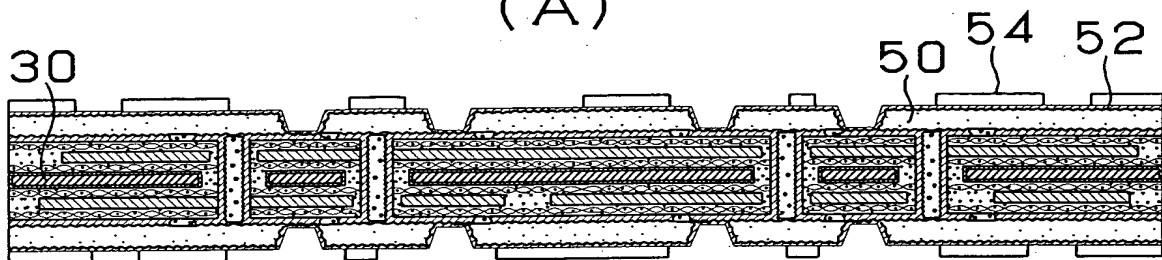
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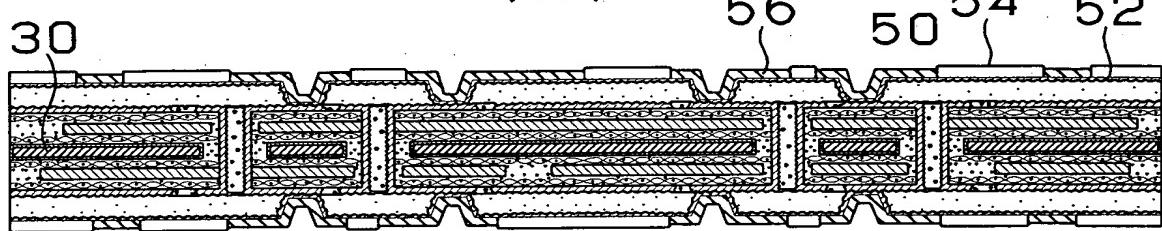
(D)



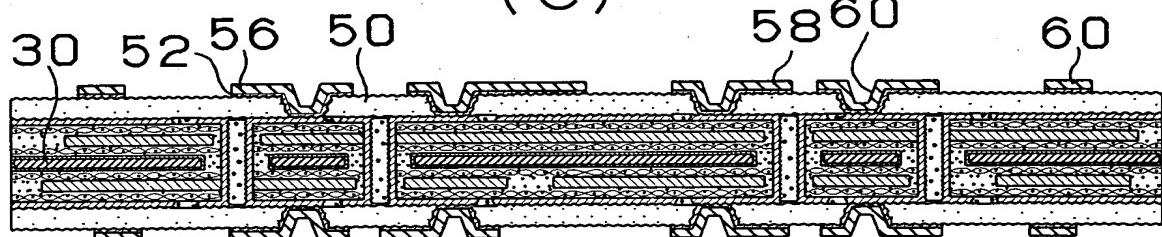
17/29
Fig. 17
(A)



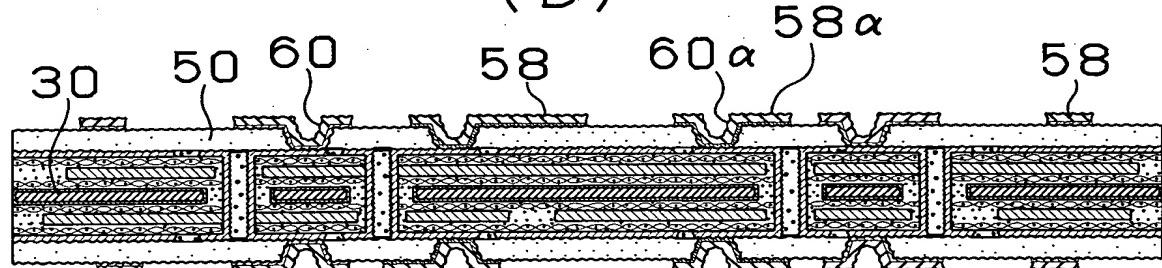
(B)



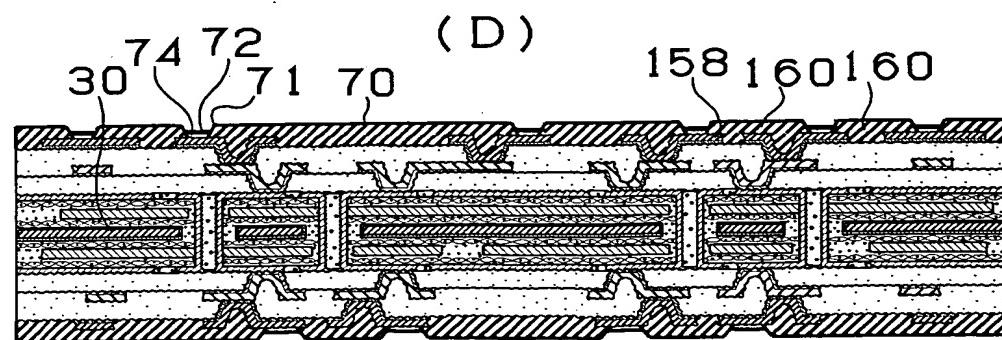
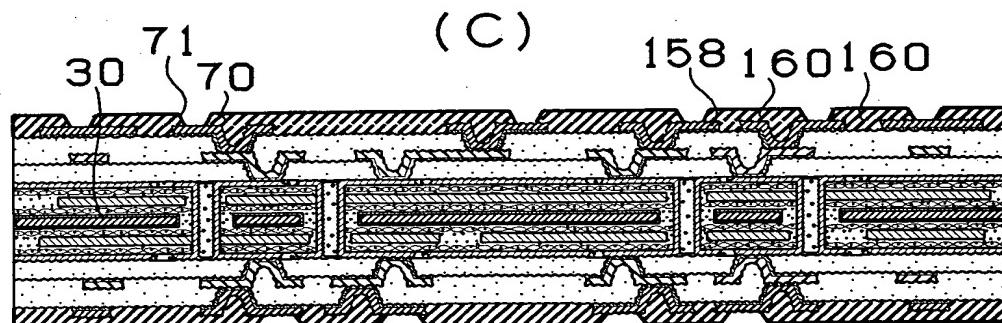
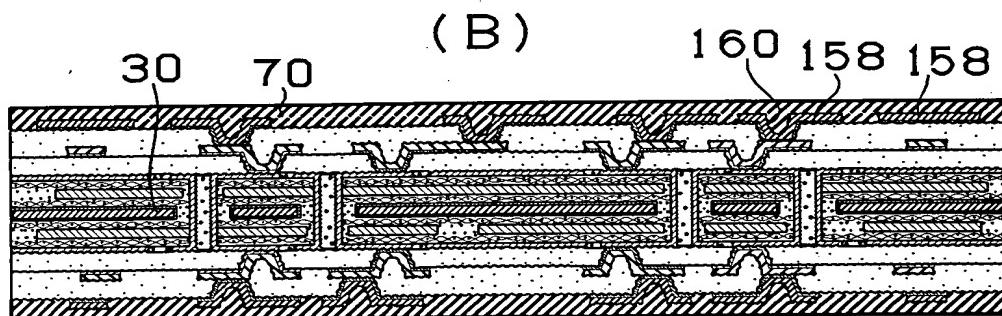
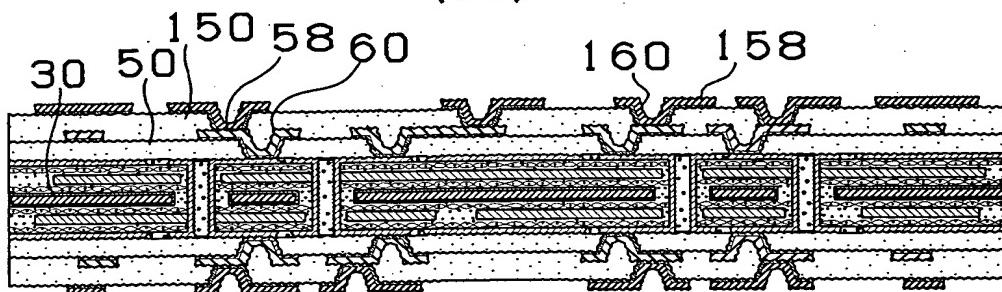
(C)



(D)



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Fig. 18
(A)



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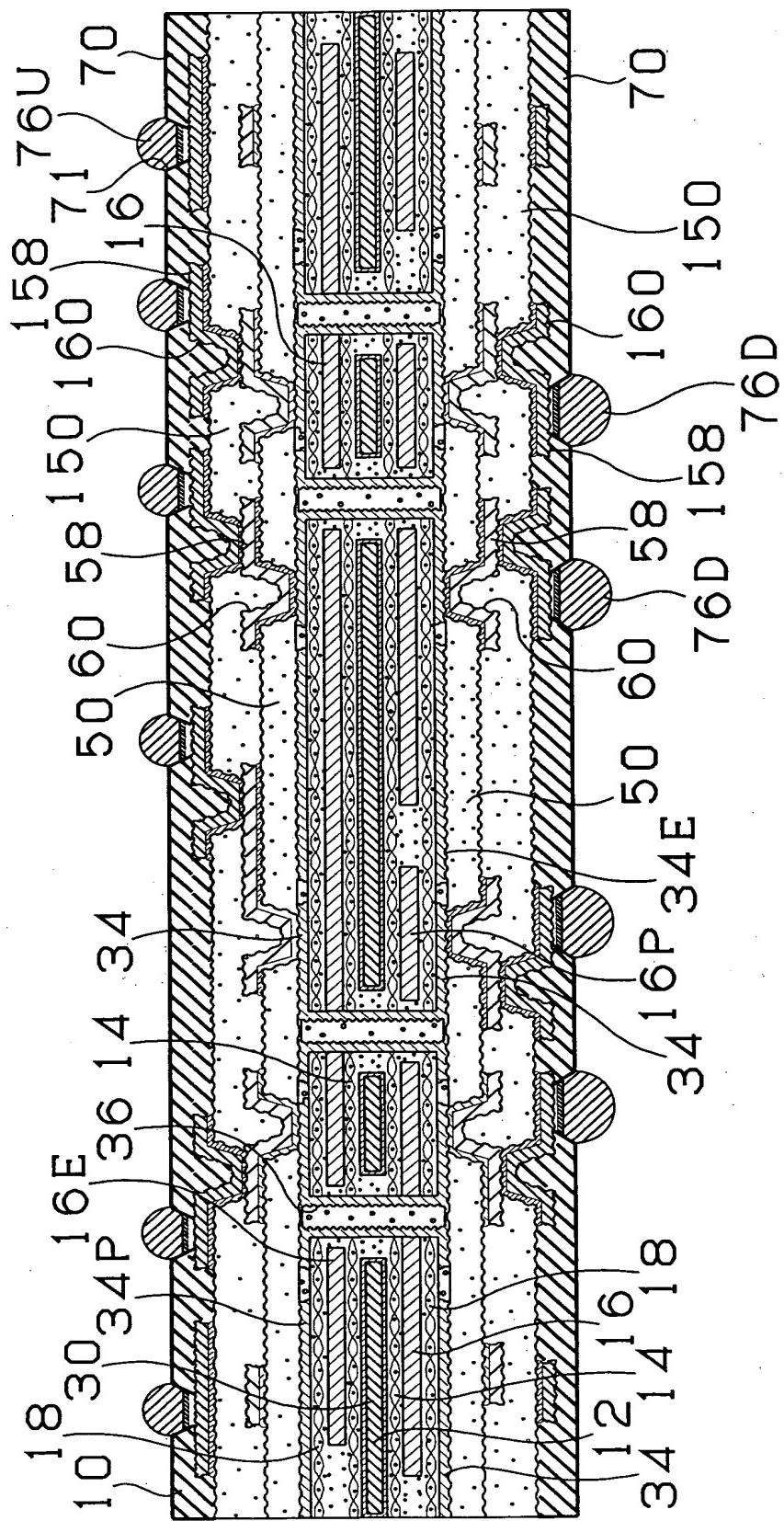
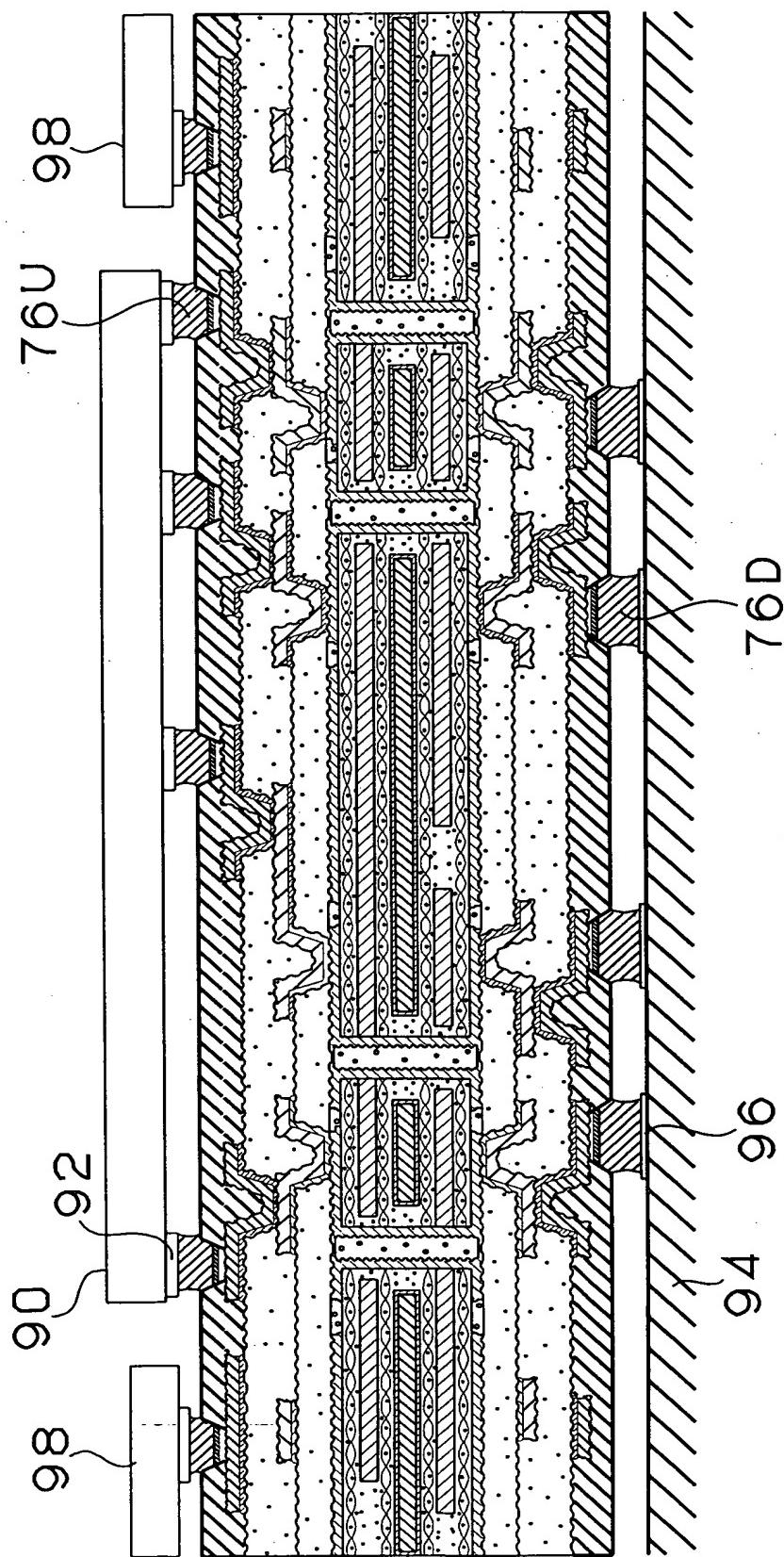


Fig. 1

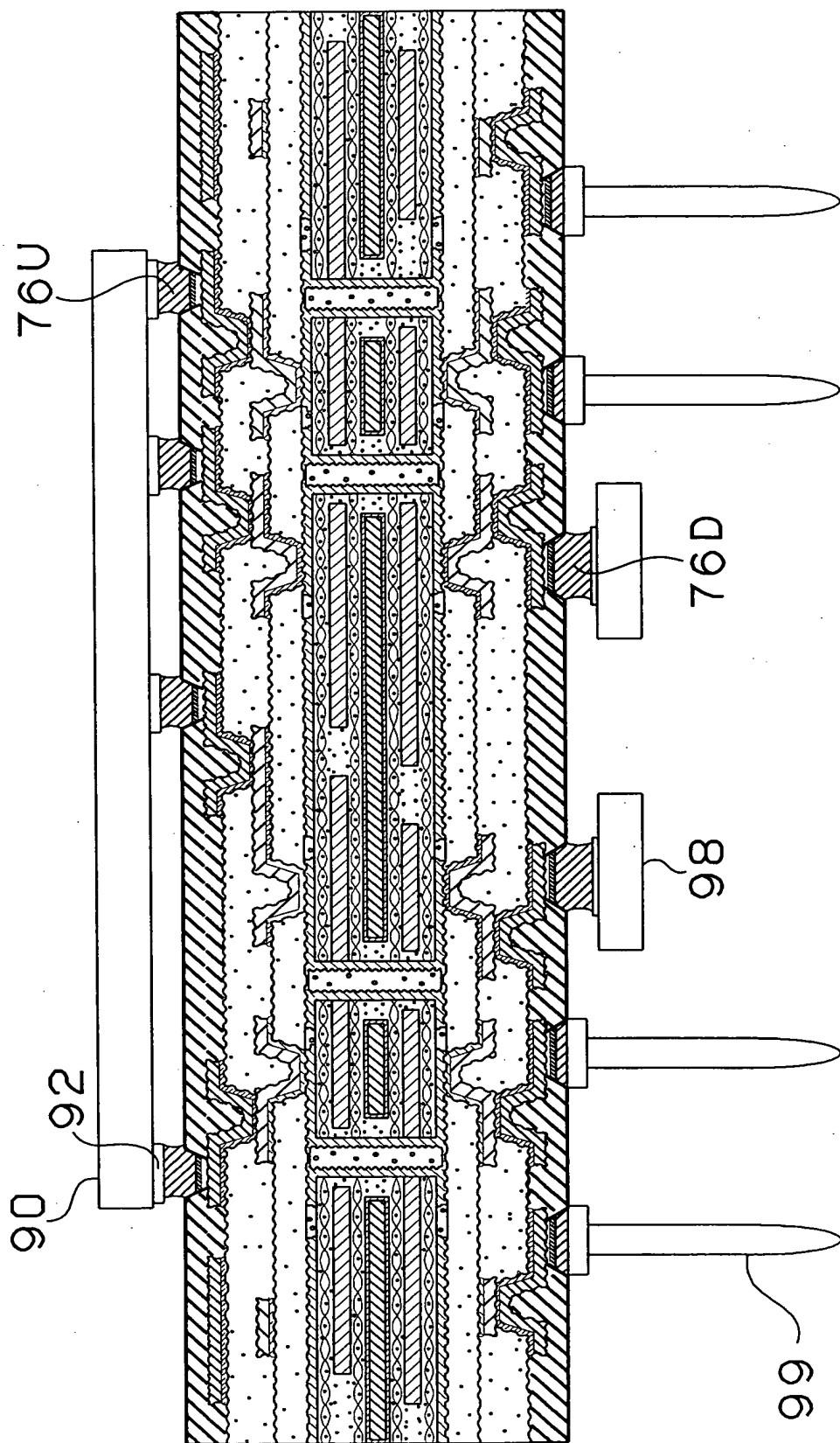
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Fig. 20



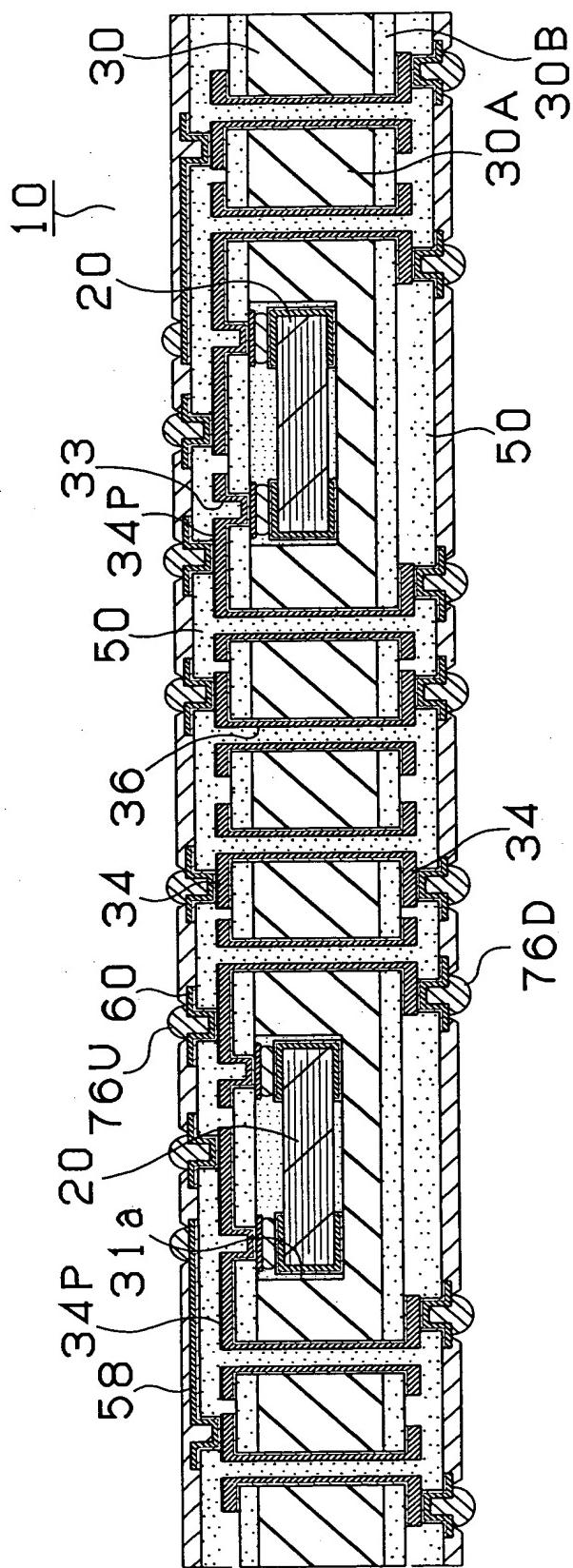
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Fig. 21



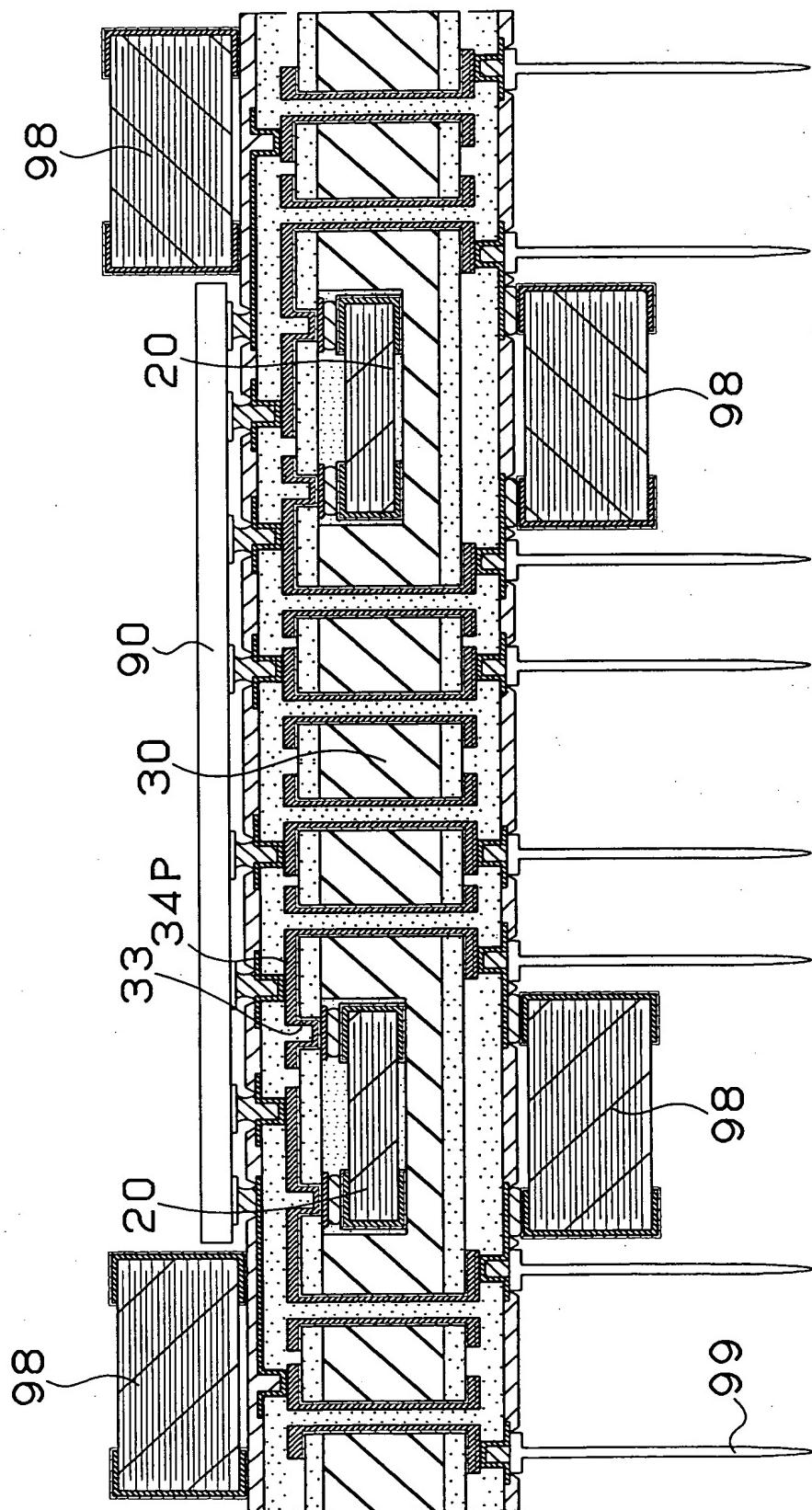
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Fig. 22



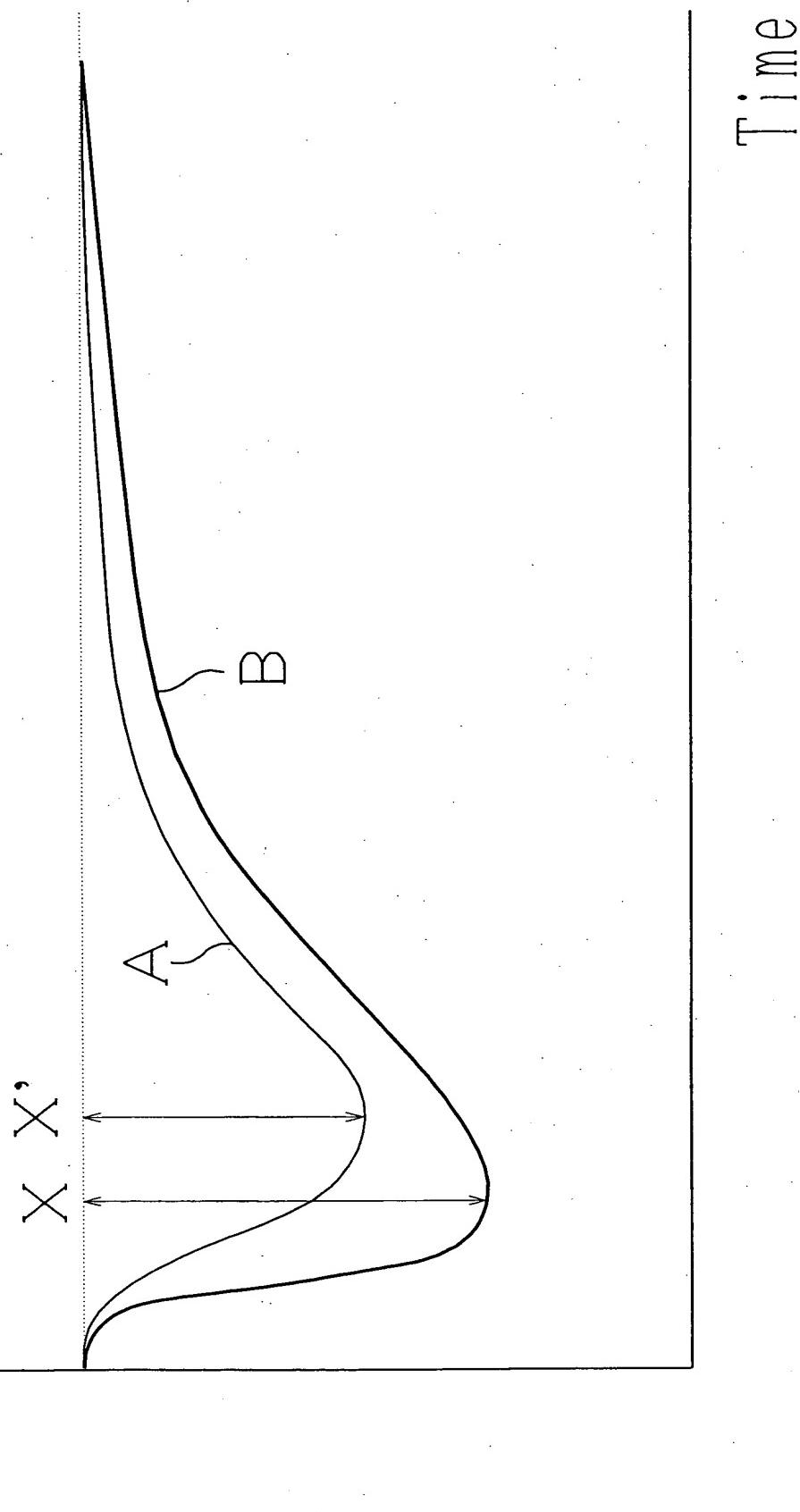
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Fig. 23



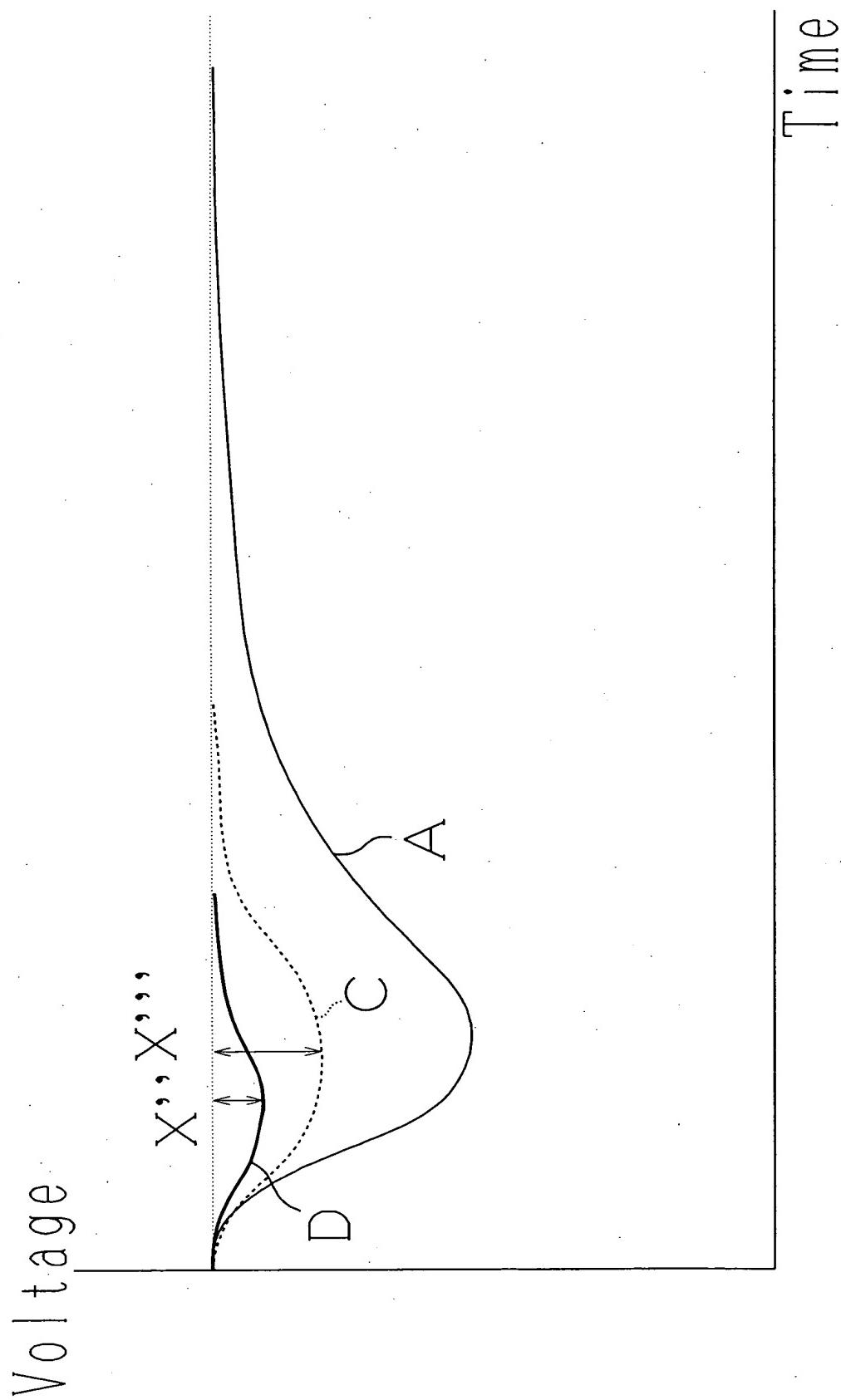
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Fig. 24



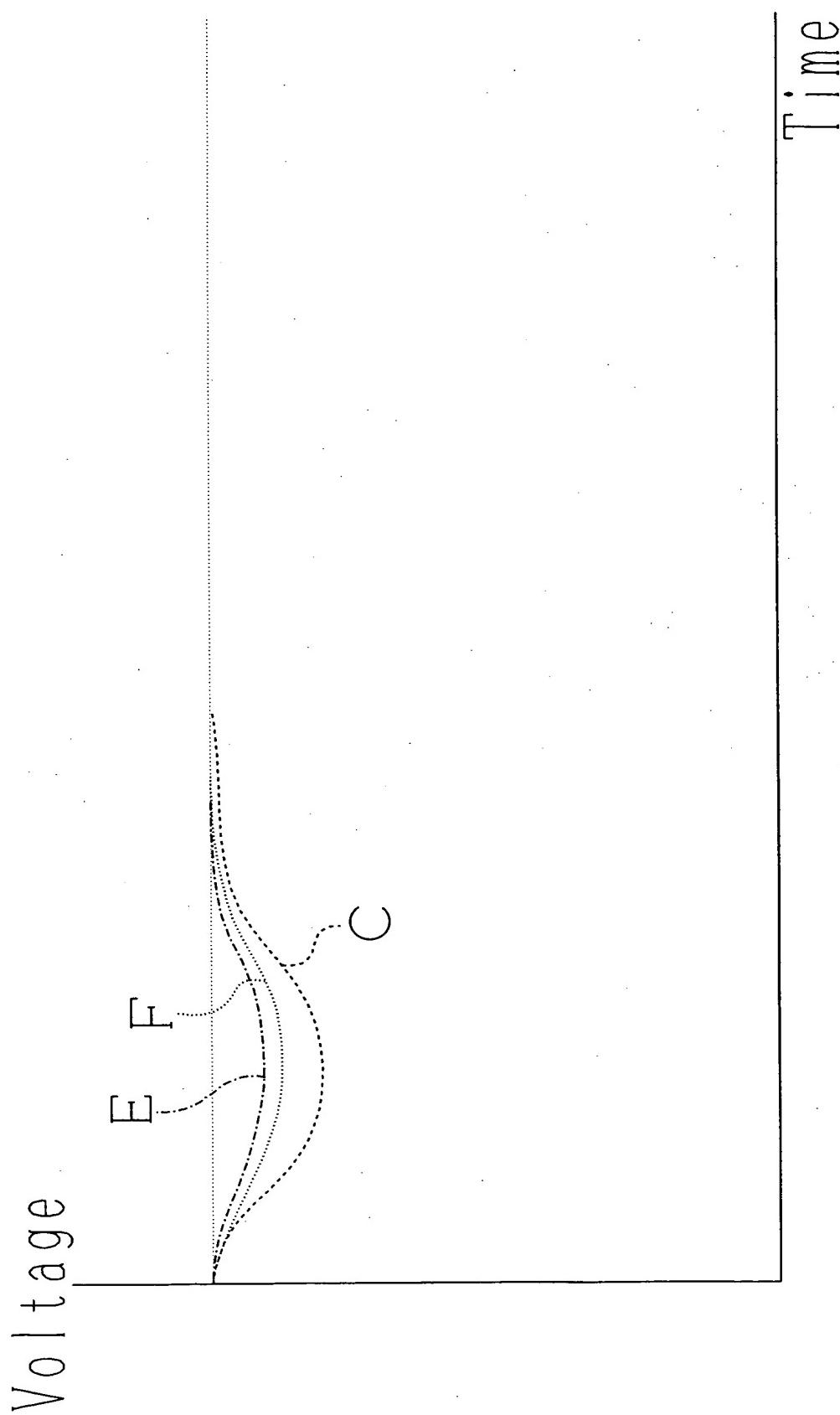
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Fig. 25



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Fig. 26



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Fig. 27

	Thickness of Conductor Circuit	1 0 0 h r	3 0 0 h r	5 0 0 h r	1 0 0 0 h r	Voltage Drop Quantity
	Ratio α_1/α_2	Presence/Absence of Malfunction of IC	Presence/Absence of Open	Presence/Absence of Malfunction of IC	Presence/Absence of Open	Presence/Absence of Malfunction of IC
Embodiment 1-1	2.0	○	○	○	○	○
Embodiment 1-2	3.7	○	○	○	○	○
Embodiment 1-3	5.0	○	○	○	○	○
Embodiment 1-4	30.0	○	○	○	○	○
Embodiment 1-5	1.2	○	○	○	○	○
Embodiment 2-1	2.0	○	○	○	○	○
Embodiment 2-2	3.7	○	○	○	○	○
Embodiment 2-3	5.0	○	○	○	○	○
Embodiment 2-4	30.0	○	○	○	○	○
Embodiment 3-1	2.3	○	○	○	○	○
Embodiment 3-2	3.7	○	○	○	○	○
Embodiment 3-3	10.0	○	○	○	○	○
Embodiment 3-4	30.0	○	○	○	○	○
Embodiment 3-5	40.0	○	○	○	○	○
Embodiment 4-1	3.3	○	○	○	○	○
Embodiment 4-2	4.0	○	○	○	○	○
Embodiment 4-3	5.0	○	○	○	○	○
Embodiment 4-4	20.0	○	○	○	○	○
Embodiment 4-5	30.0	○	○	○	○	○
Embodiment 4-6	40.0	○	○	○	○	○

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Fig. 28

	Thickness of Conductor Circuit	1 0 h r	3 0 0 h r	5 0 0 h r	1 0 0 h r	Voltage Drop Quantity
	Ratio α_1 / α_2	Presence/Absence of Malfunction of IC	Presence/Absence of Open	Presence/Absence of Malfunction of IC	Presence/Absence of Open	Presence/Absence of Malfunction of IC
Embodiment 5-1	6.7	○	○	○	○	○
Embodiment 5-2	5.4	○	○	○	○	○
Embodiment 5-3	10.0	○	○	○	○	○
Embodiment 5-4	20.0	○	○	○	○	○
Embodiment 5-5	30.0	○	○	○	○	○
Embodiment 5-6	40.0	○	○	○	○	○
Embodiment 6-1	2.0	○	○	○	○	○
Embodiment 6-2	3.7	○	○	○	○	○
Embodiment 6-3	5.0	○	○	○	○	○
Embodiment 6-4	30.0	○	○	○	○	○
Comparison Example	1.0	×	○	○	×	×
Reference Example	41.5	○	○	○	○	○

Presence/Absence of Malfunction of IC, (○ : No Malfunction; × : Malfunction, Presence/Absence of Open, ○ : No Open, × : Open)

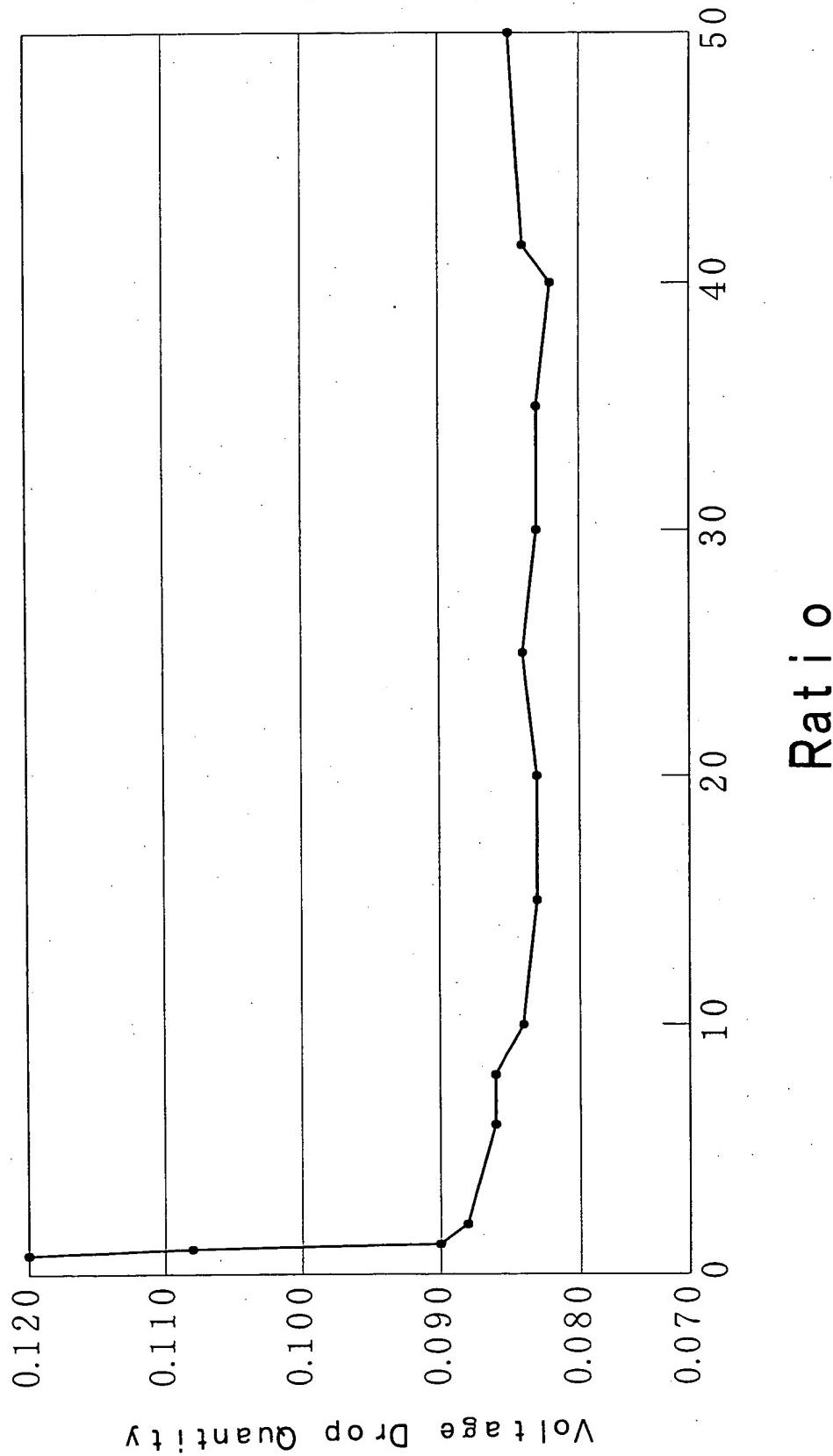
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Fig. 29

Core Power Supply Layer Ratio



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